

A

A

B

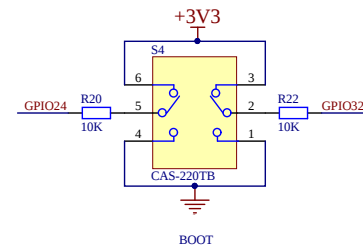
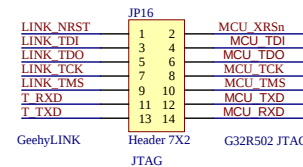
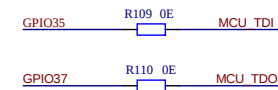
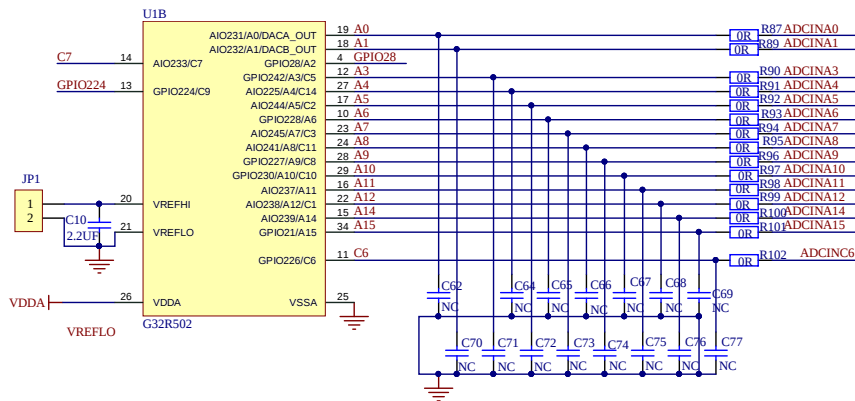
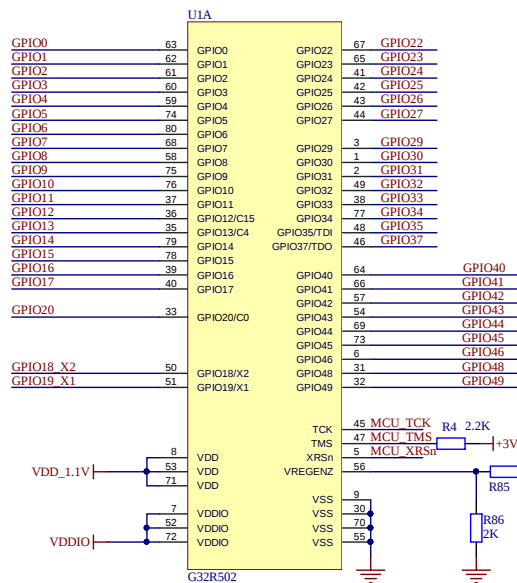
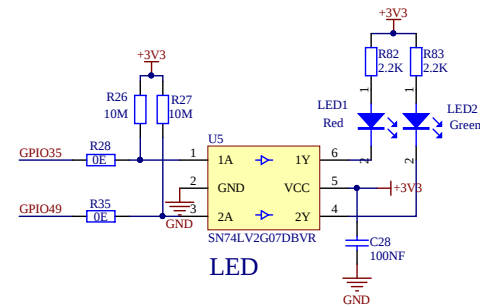
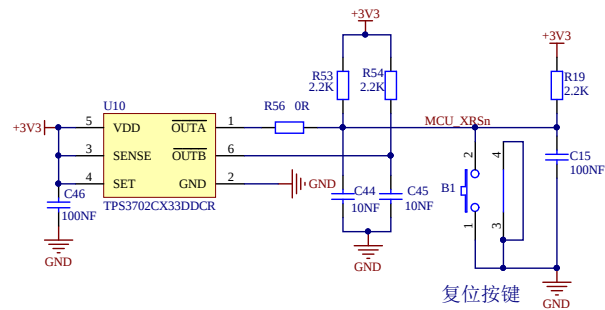
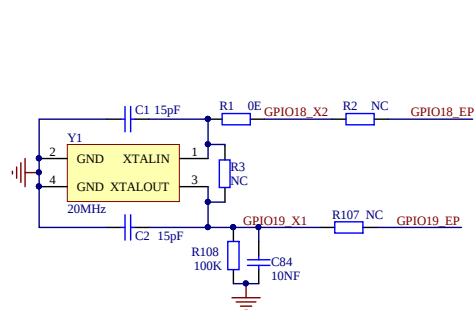
B

C

C

D

D



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A

B

C

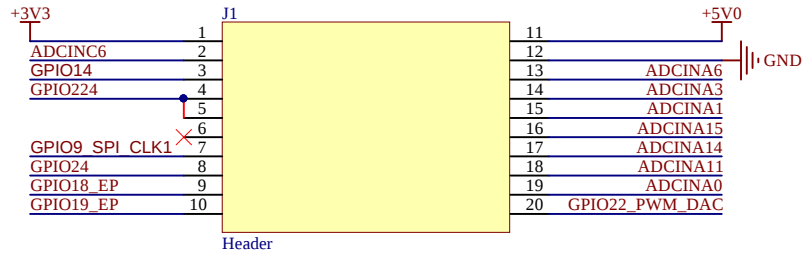
D

A

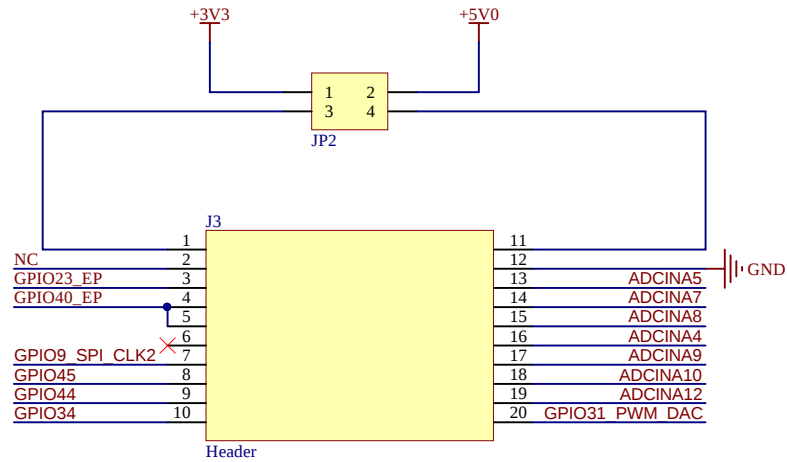
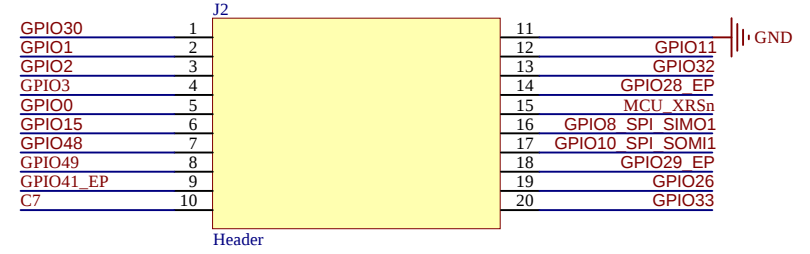
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C

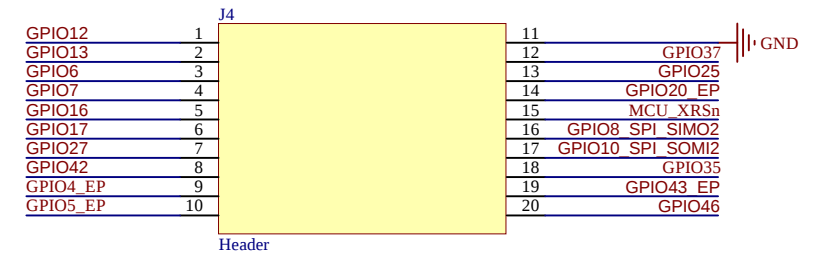
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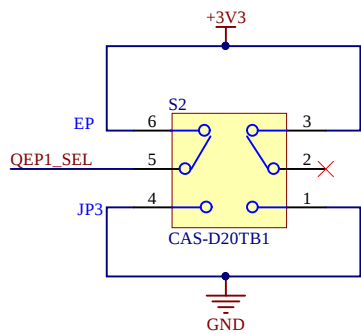
ExpandPack Header Site 1



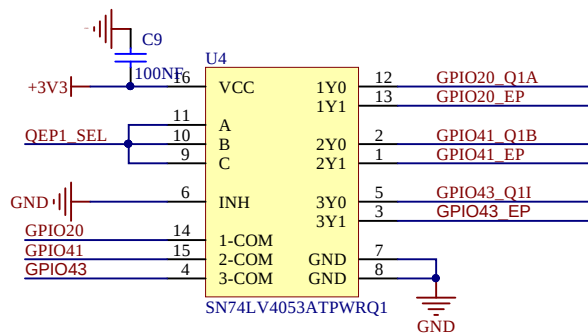
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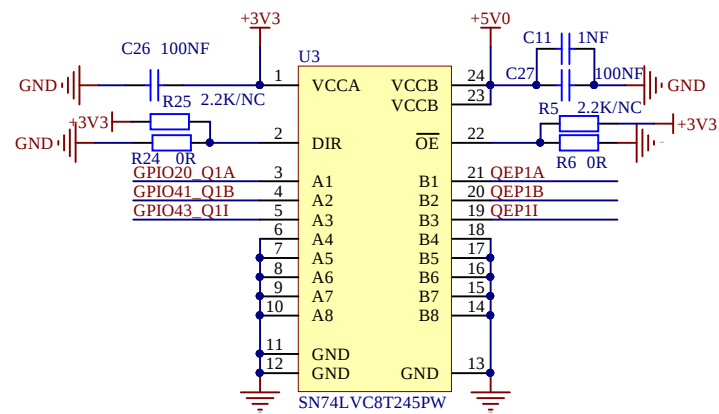
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	DocumentName	ExpandPack.SchDoc		
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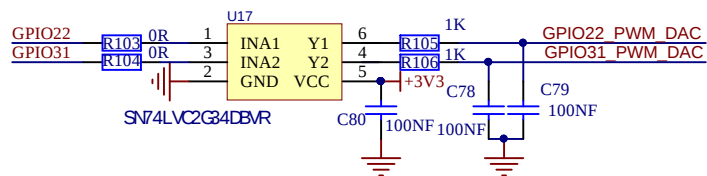
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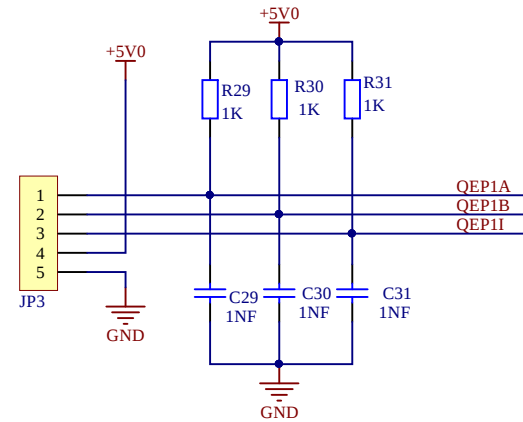
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QEP电平转换



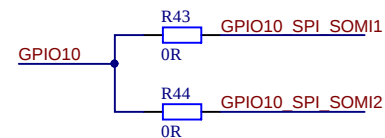
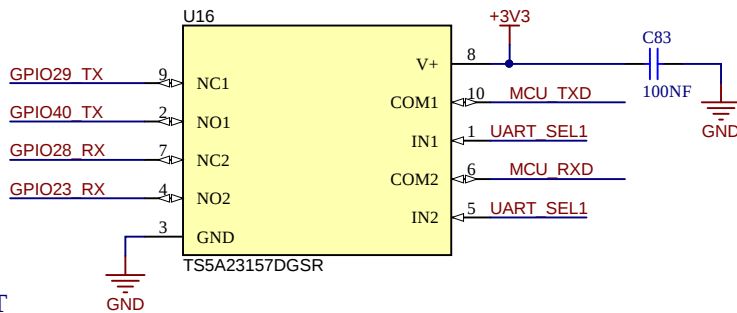
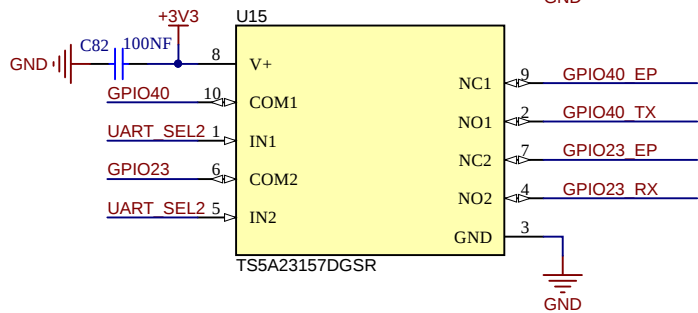
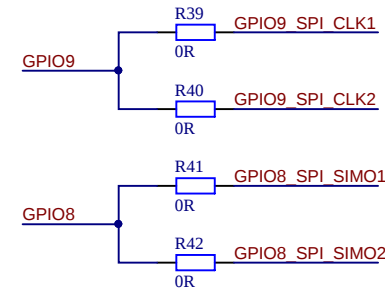
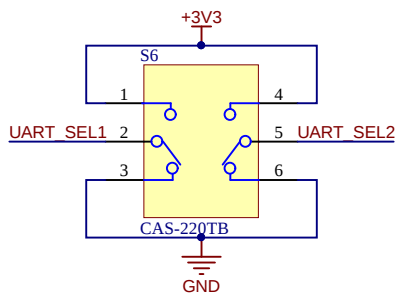
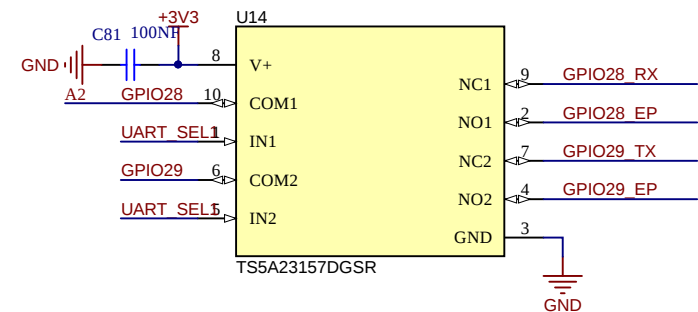
PWM-DAC



QEP

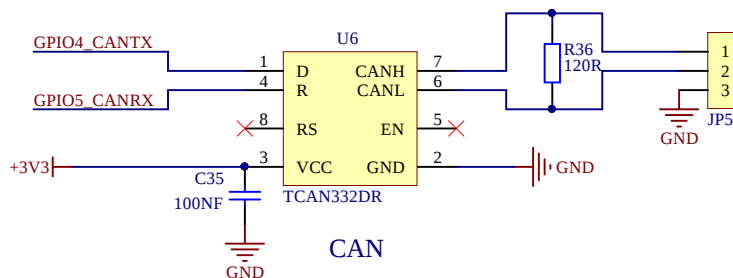
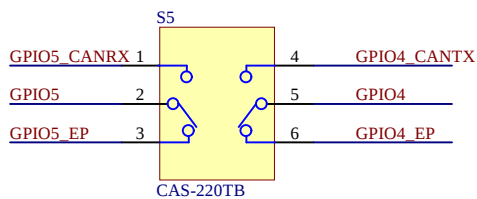
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	DocumentName	QEP&PWM-DAC.SchDoc		
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UART_SEL1	UART_SEL2	GPIO28/29	GPIO23/40
0	0	COM Port	EP
0	1	COM Port	NC
1	0	EP	EP
1	1	EP	COM Port



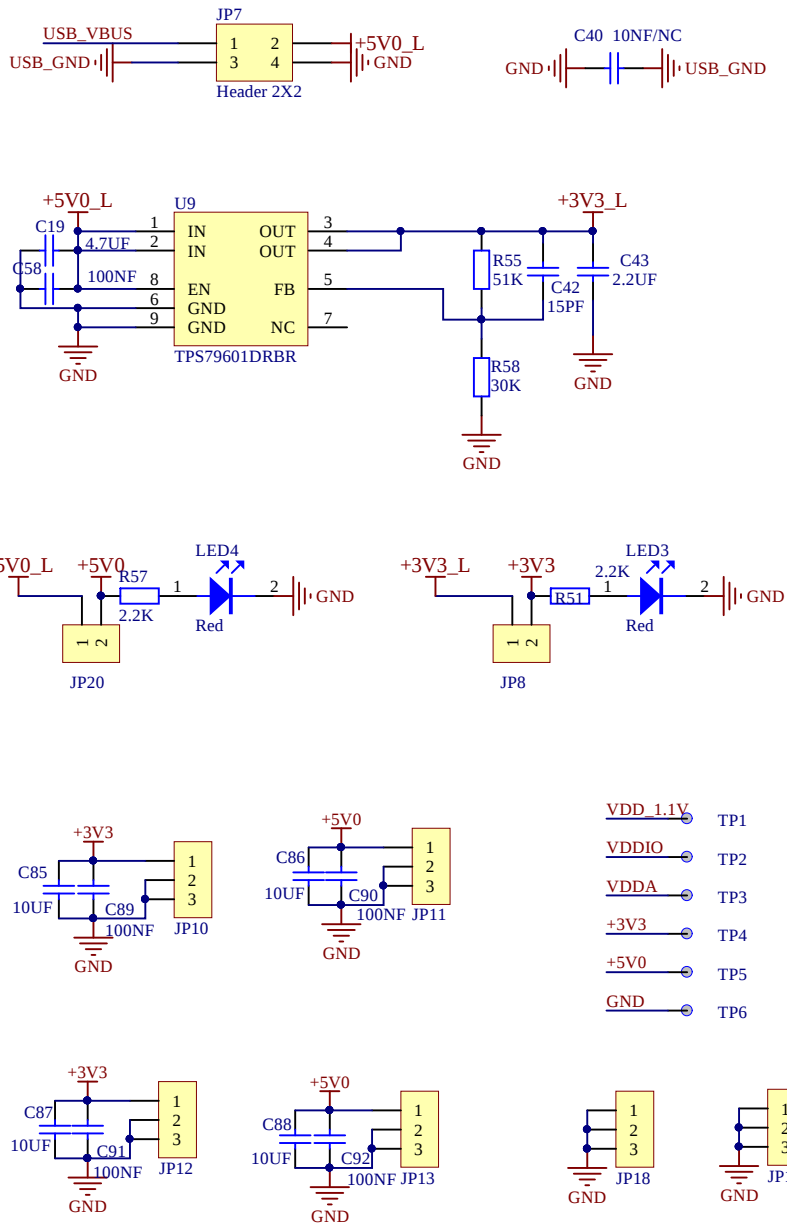
SPI

UART

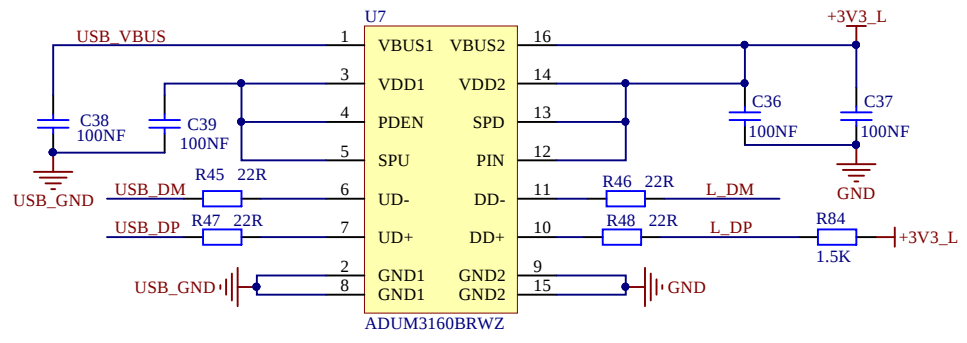
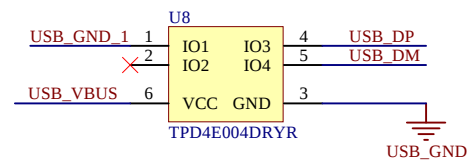
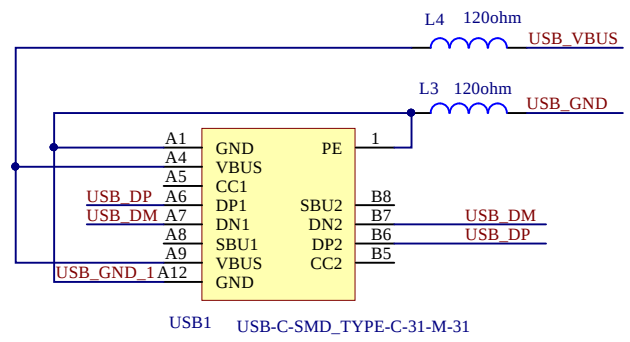


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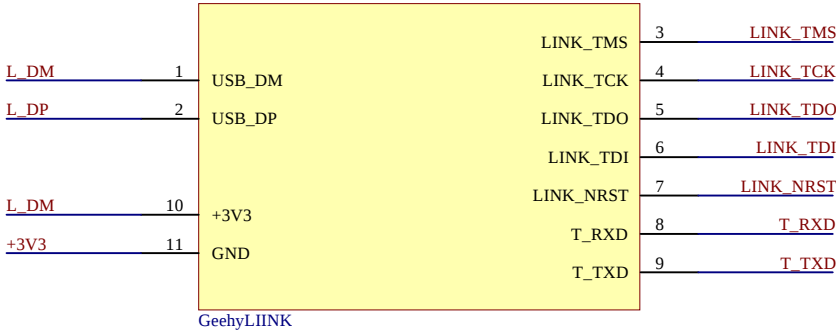
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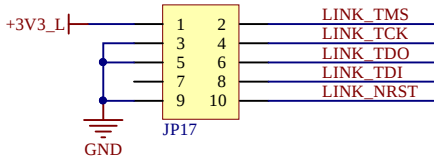
- VDD 1.1V TP1
- VDDIO TP2
- VDDA TP3
- +3V3 TP4
- +5V0 TP5
- GND TP6



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GeehyLINK



Geehy

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